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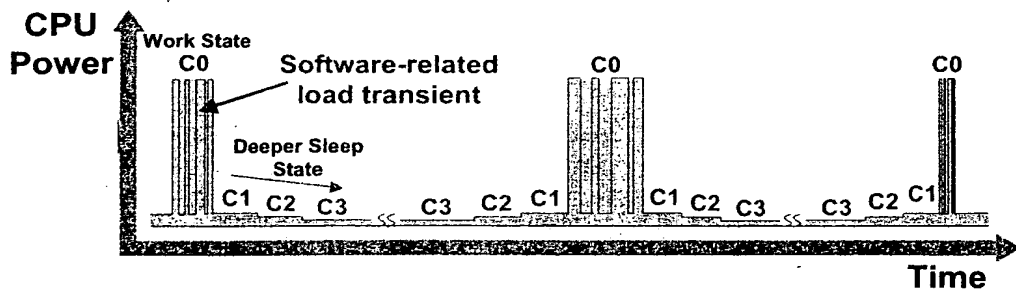


FIGURE 1

Measured Overall Efficiency vs. V_{bus} at Different Loads

($V_{in}=16V$; 1MHz; With Drive Loss)

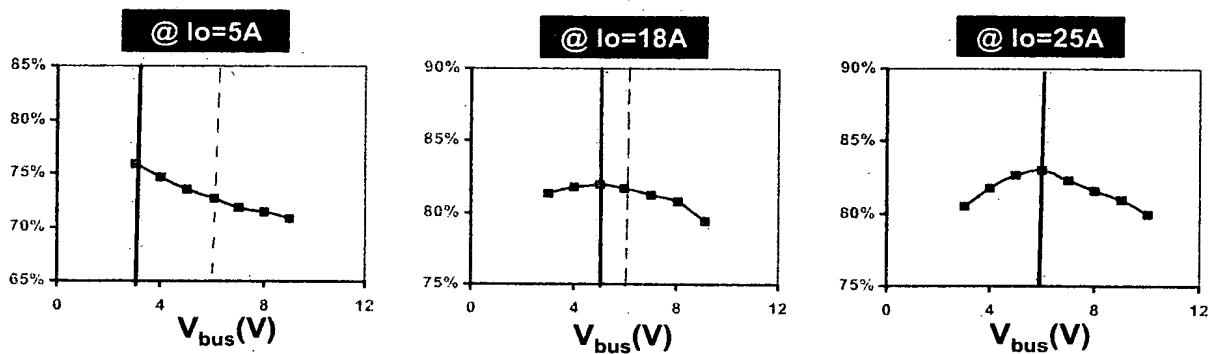


FIGURE 2

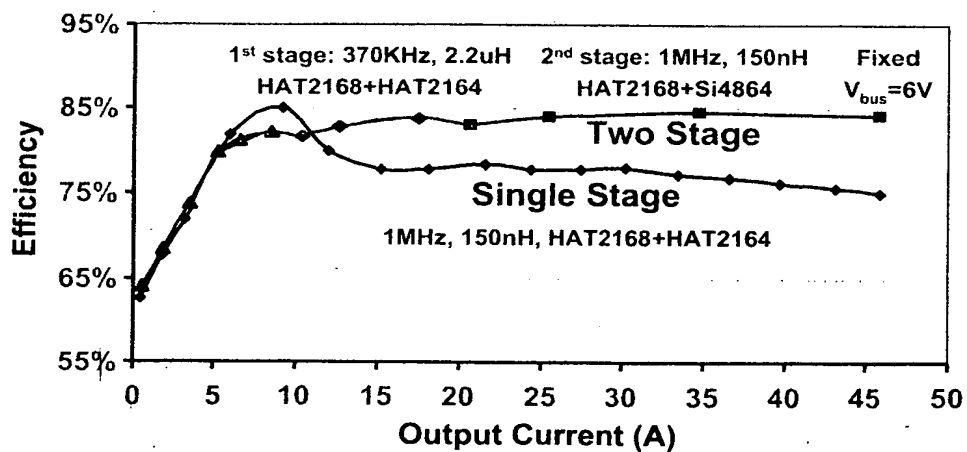


FIGURE 3A

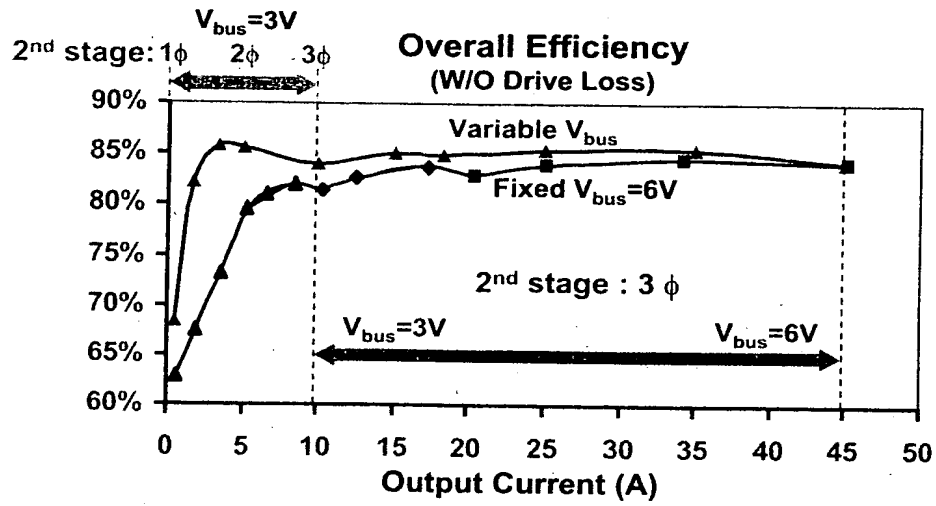


FIGURE 3B

Global System Power States and Transitions

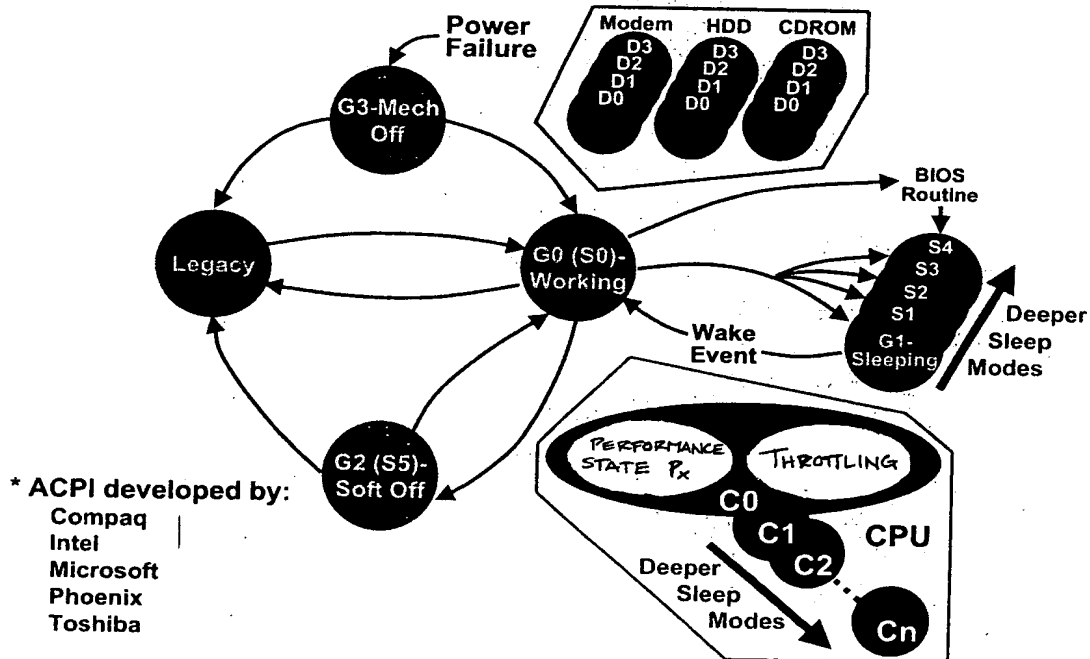


FIGURE 4

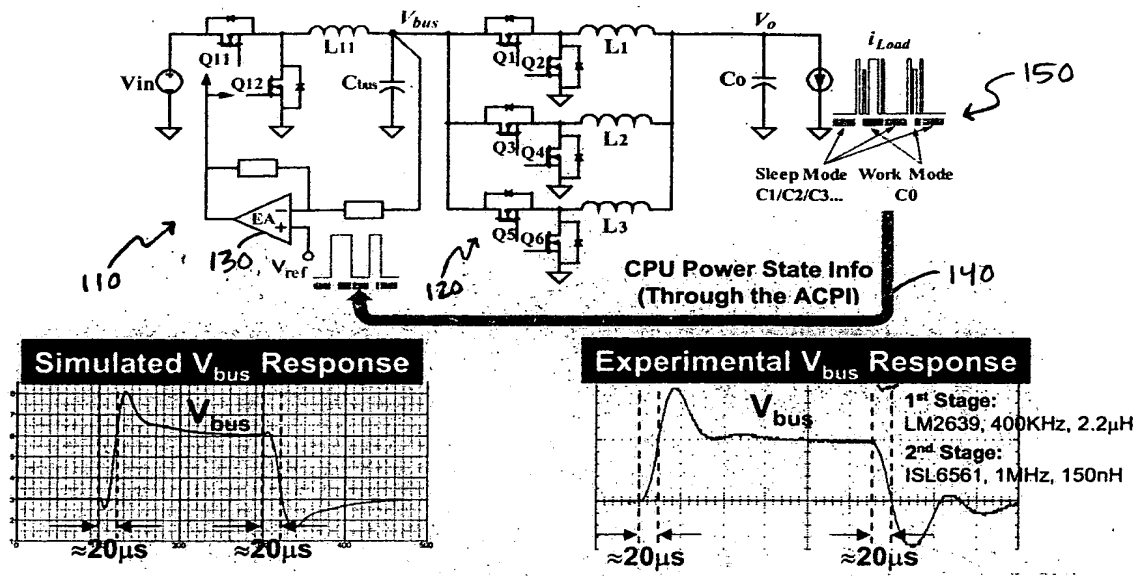


FIGURE 5

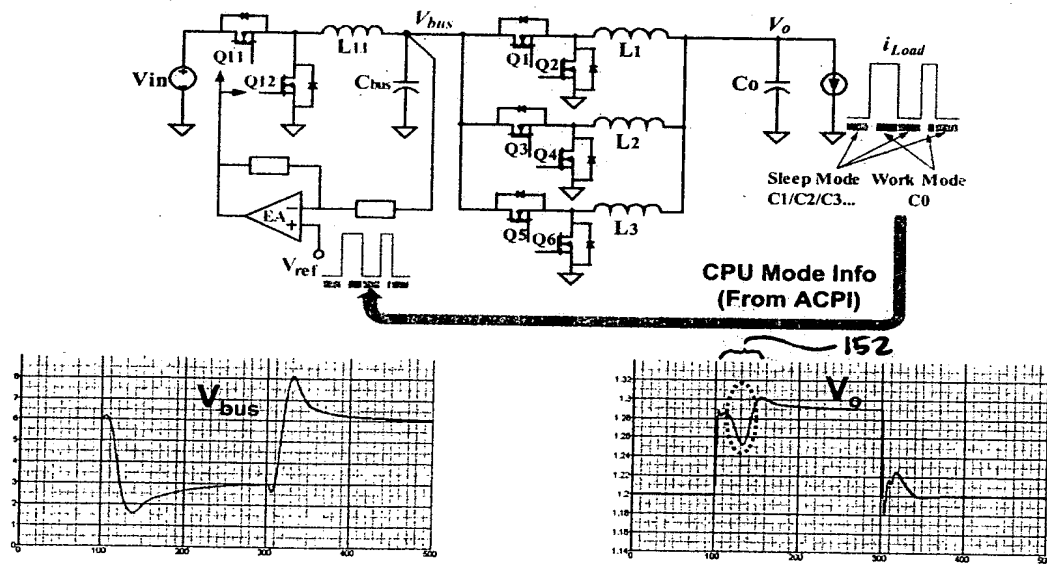
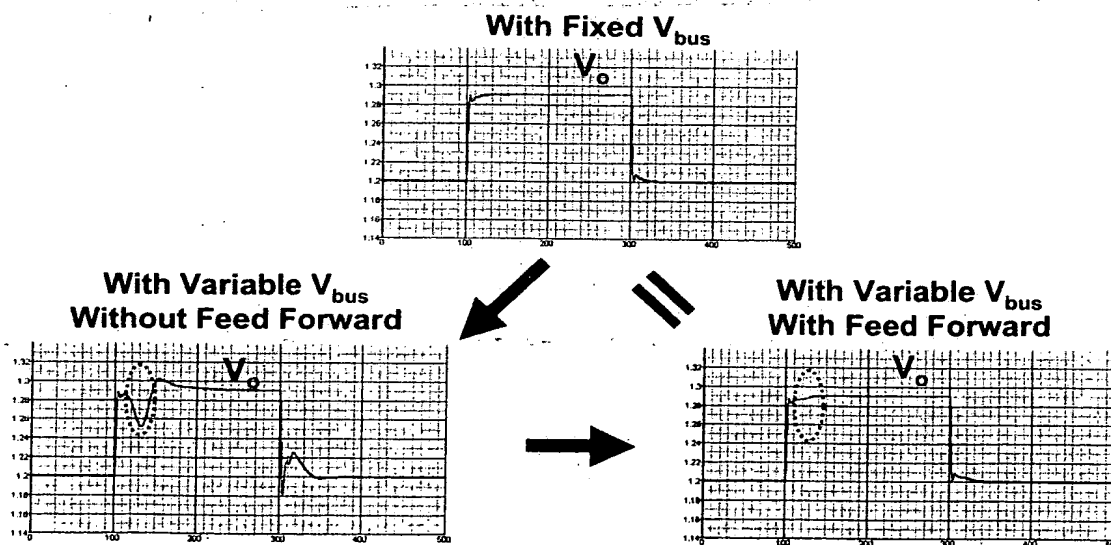
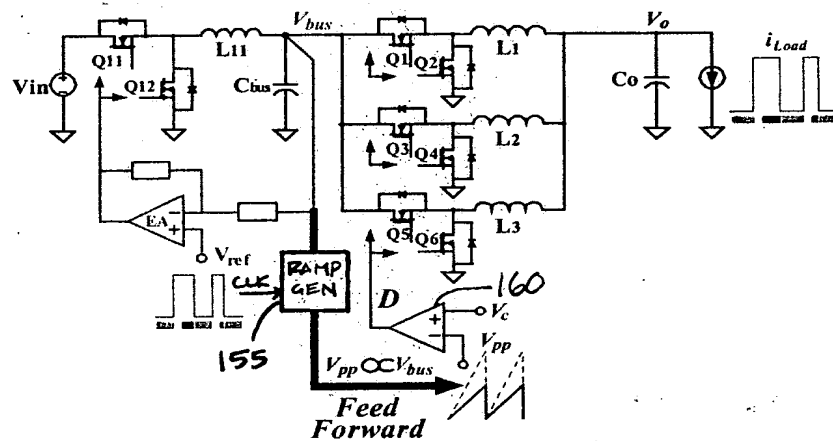
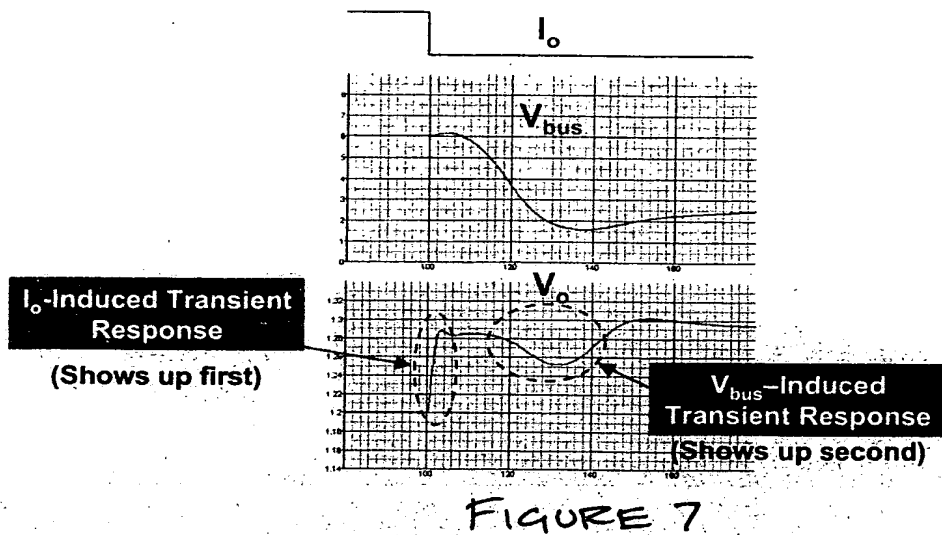


FIGURE 6



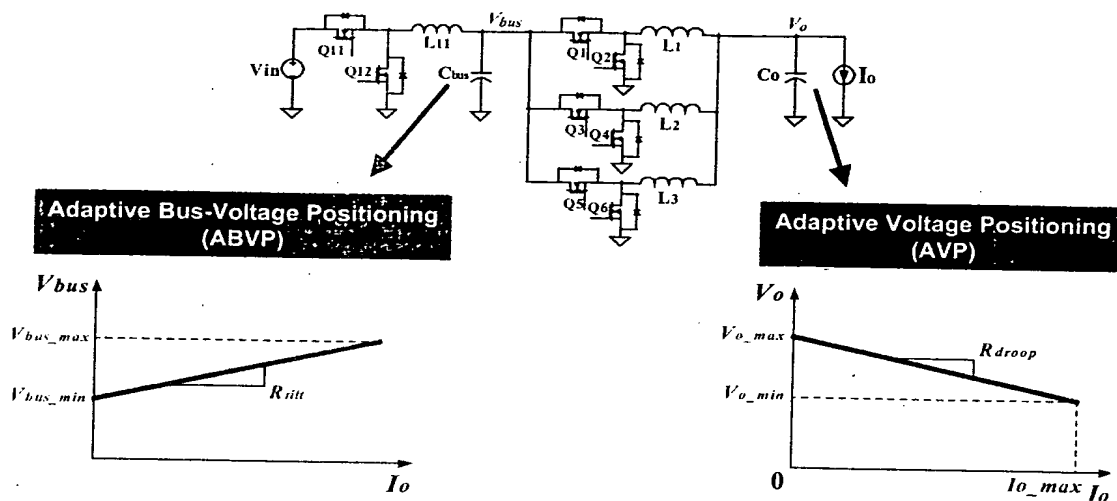


FIGURE 10A

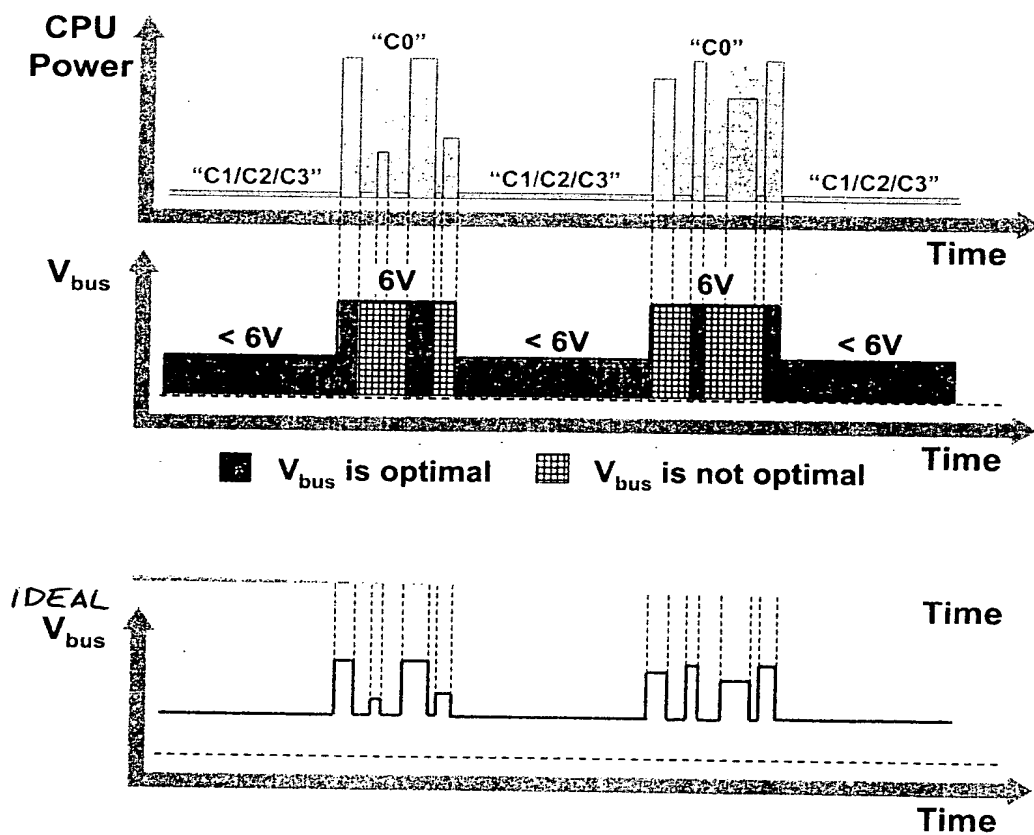
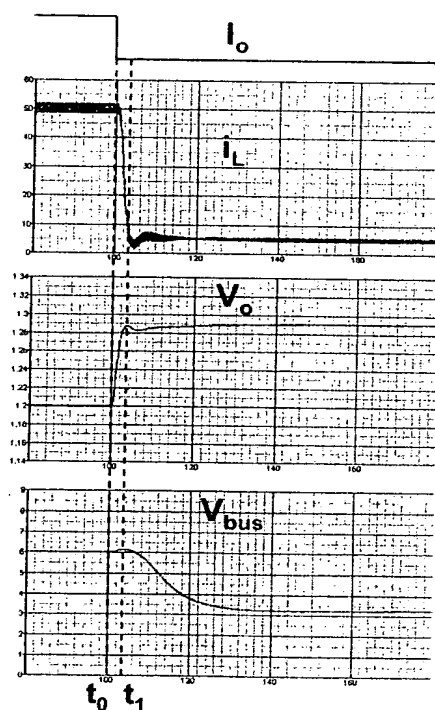
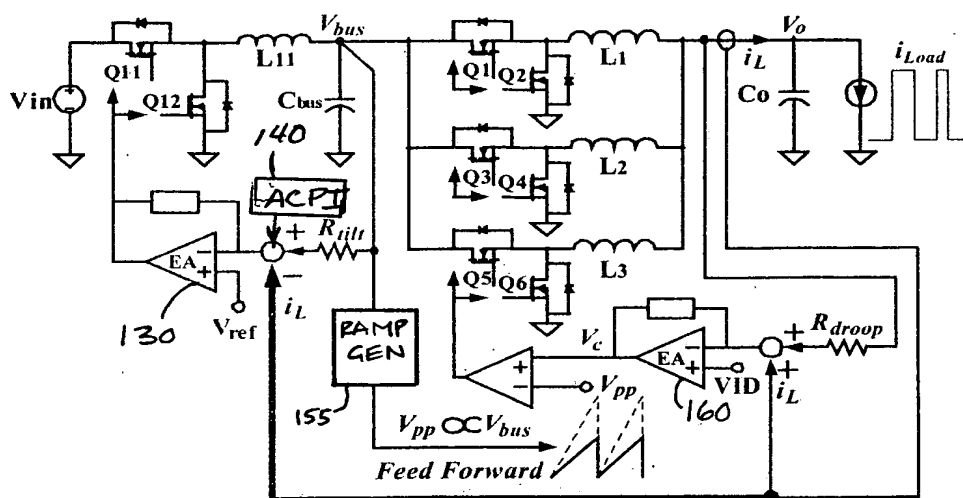


FIGURE 10B



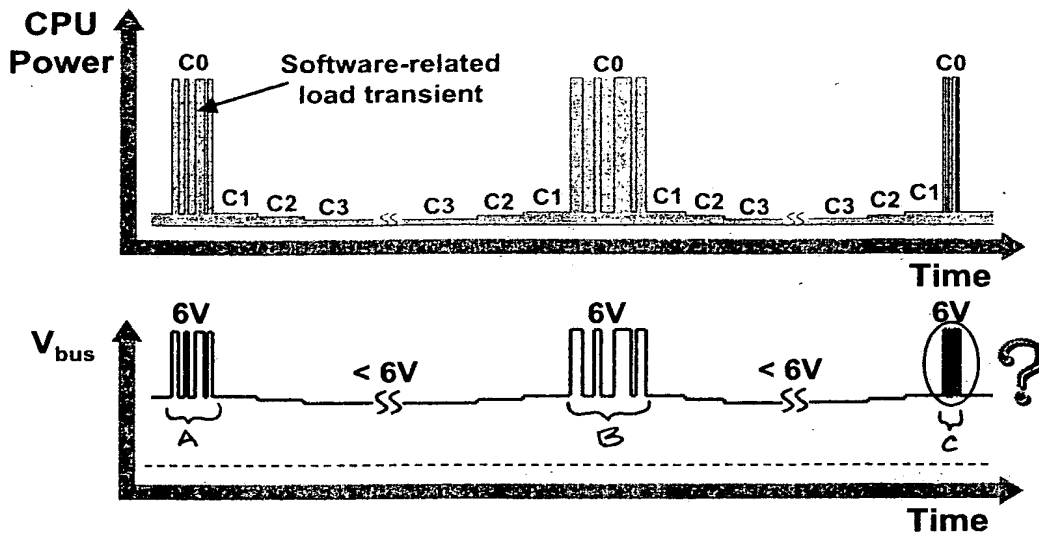


FIGURE 13

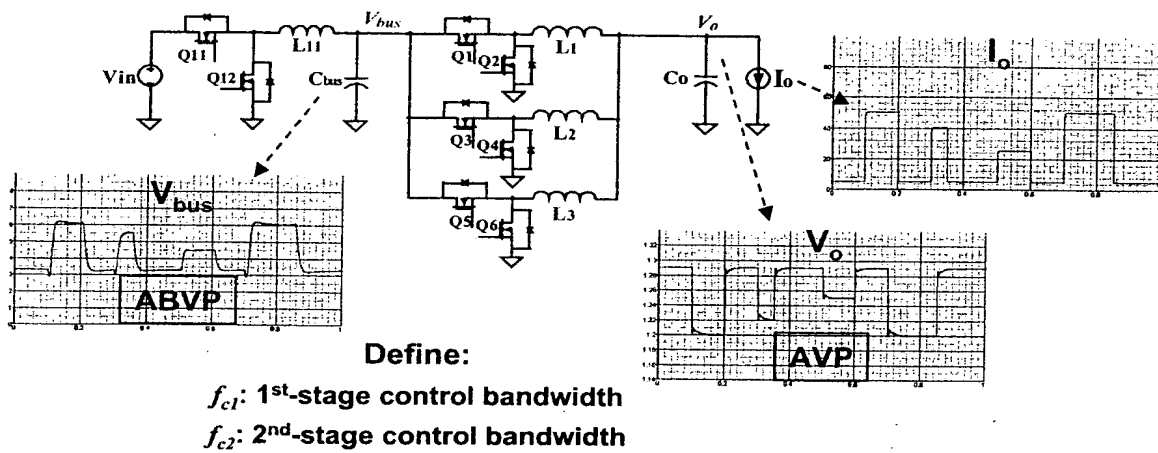


FIGURE 14A

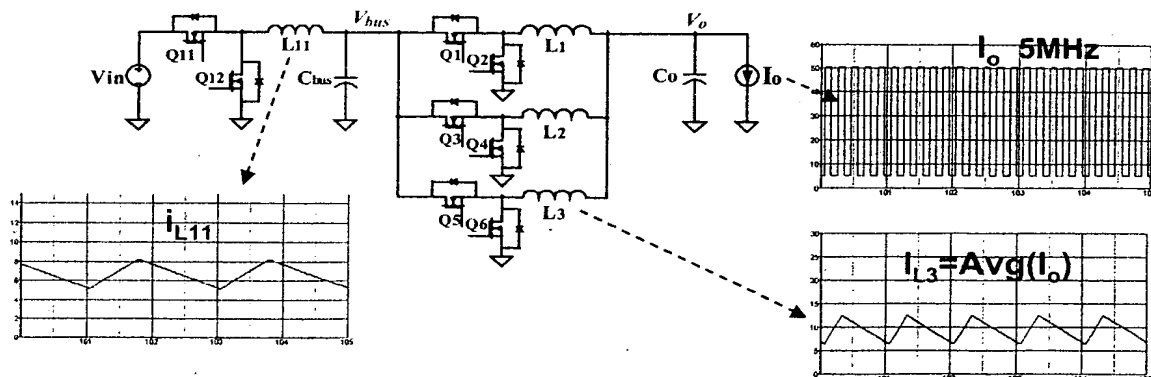


FIGURE 14B

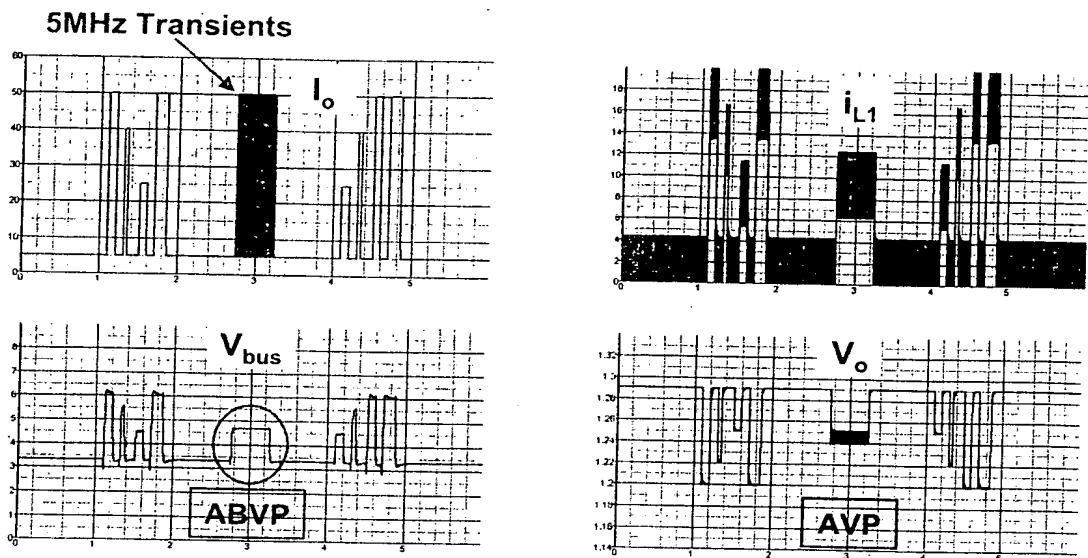


FIGURE 14C

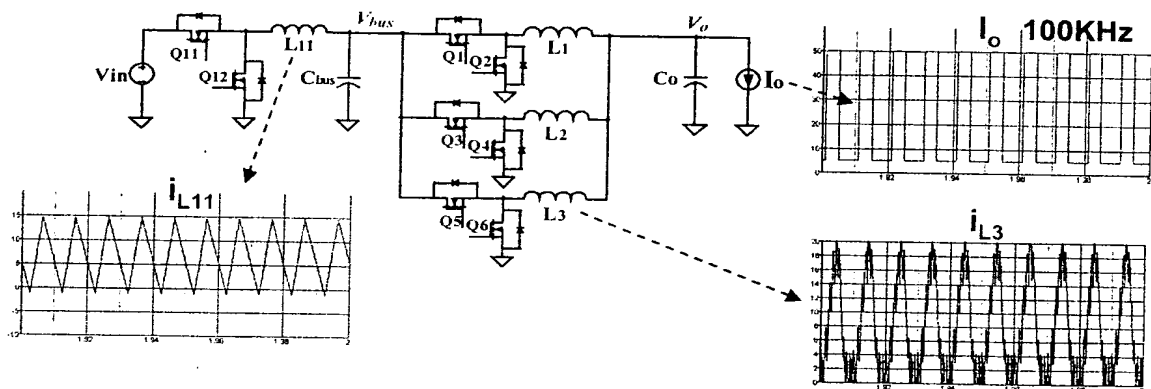


FIGURE 14D

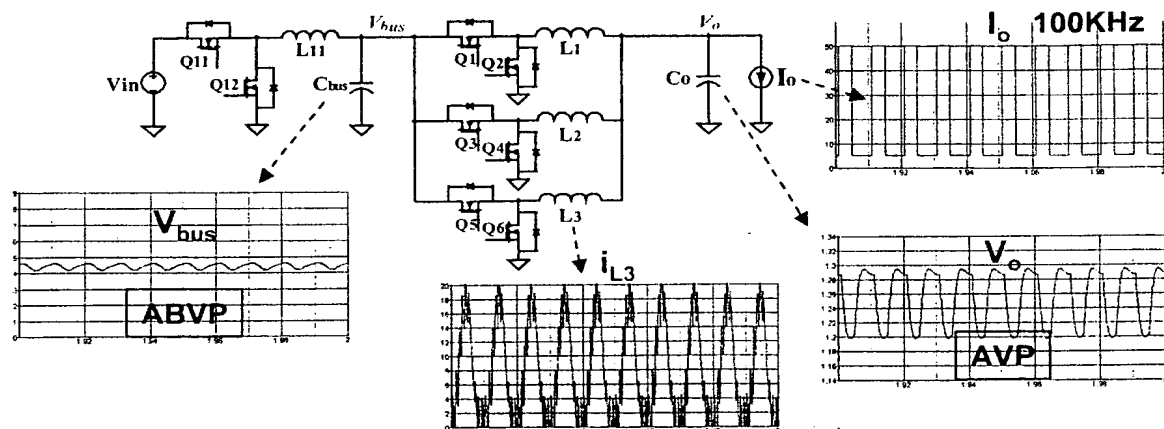


FIGURE 15